

**IN THE SPECIFICATION:**

On page 2, after line 3, please insert the following paragraph:

**SUMMARY OF DISCLOSURE:**

According to one embodiment, a system for disabling a computer bus upon detection of a power fault includes a bus bridge device coupled to a bus and a power regulator that delivers power to the bus. If the power regulator detects a power fault, then the power regulator asserts a fault signal to the bus bridge device. The power regulator also removes power from the bus. The bus bridge device disconnects an internal logic unit from the bus in response to the assertion of the fault signal. The bus bridge device, in further response to the assertion of the fault signal, alerts the system of the power fault by asserting an interrupt signal. In a particular embodiment, the bus is a secondary PCI bus and the bus bridge device is a PCI/PCI bridge bridging the secondary PCI bus to a primary PCI bus coupled to a processor via a system logic device. In response to the fault signal, the PCI/PCI bridge is configured to assert an error signal to the system logic device regarding a fault condition associated with the fault signal.